

[Advanced Search](#) [Preferences](#) [Language Tools](#) [Search Tips](#)

embedded tracing

Google Search

[Web](#) · [Images](#) · [Groups](#) · [Directory](#) · [News](#)Searched the web for **embedded tracing**.Results **1 - 10** of about **58,200**. Search took **0.29** seconds.

2. Tracing the evolution of deeply **embedded** massive young stars

... Detection of interstellar H 2 D + 2. **Tracing** the evolution of deeply **embedded** massive young stars. Van der Tak, in collaboration with ...
www.strw.leidenuniv.nl/annrep99/node53.html - 6k - [Cached](#) - [Similar pages](#)

[astro-ph/9706082] **Tracing** the envelopes around **embedded** low-mass ...

... 485kb) **Tracing** the envelopes around **embedded** low-mass young stellar objects with HCO+ and millimeter-continuum observations. Authors ...
arxiv.org/abs/astro-ph/9706082 - 4k - [Cached](#) - [Similar pages](#)

Diagnostic Testing of **Embedded** Memories Based on Output **Tracing**

... Testing (MTDT'00) August 07 - 08, 2000 San Jose, California. p. 113 Diagnostic Testing of **Embedded** Memories Based on Output **Tracing**. PDF. ...
csdl.computer.org/comp/proceedings/ mtdt/2000/0689/00/06890113abs.htm - 10k - [Cached](#) - [Similar pages](#)

SpyKer Trace Tool: Selective Event **Tracing**. Buffering for Crash ...

... SpyKer: a breakthrough in debugging **embedded** systems. ... you are here >, 6. Selective event **tracing** and buffering for crash analysis. How to order SpyKer. ...
www.linuxworks.com/products/spyker/spyker06.php3 - [Similar pages](#)

artwithbraininmind-l: **Tracing** the link between **embedded** art and

Tracing the link between **embedded** art and 'subliminal' advertising.
From: JimHagart@aol.com Date: Sat Jun 30 2001 - 05:30:31 EDT: ...
kh.bu.edu/artwithbraininmind-l/1355.html - 8k - [Cached](#) - [Similar pages](#)

[PDF] Diagnostic Testing of **Embedded** Memories Based on Output **Tracing**

File Format: PDF/Adobe Acrobat - [View as HTML](#)
Page 1. Diagnostic Testing of **Embedded** Memories Based on Output **Tracing** Revised version
Dirk Niggemeyer 1 Michael Redeker 2 Elizabeth M. Rudnick 1 1 Center for ...
www.tet.uni-hannover.de/papers/2000/00mred_2.pdf - [Similar pages](#)

Diagnostic Testing of **Embedded** Memories Based on Output **Tracing**

D. Niggemeyer, M. Redeker, and EM Rudnick. Diagnostic Testing of **Embedded** Memories Based on Output **Tracing**. Abstract. A new approach ...
www.tet.uni-hannover.de/papers/2000/00mred_2.htm - 3k - [Cached](#) - [Similar pages](#)

Telexy Networks >>> Products >>> RTTS

... This is the **tracing**/debugging tool for any **embedded** applications. ... Real time" in RTTS means that the **embedded tracing** capability allows the developer to watch ...
www.telexy.com/products/devtools/rtts/ - 15k - [Cached](#) - [Similar pages](#)

ELJonline: **Tracing** Real-Time Application Interface Using the Linux ...

... such necessary capability is the ability to trace an **embedded**/real-time ... sister publication, Linux Journal], LTT provides a framework for RTAI event **tracing**. ...
www.linuxdevices.com/articles/AT8839213109.html - 36k - Jan 12, 2004 - [Cached](#) - [Similar pages](#)

Red Hat -- Linux, **Embedded** Linux and Open Source Solutions

... Re: [redhat-ccm-list] **tracing** a DataQuery. ... Subject: Re: [redhat-ccm-list] **tracing** a DataQuery; Date: Sun, 20 Oct 2002 00:30:15 +0800. ...
https://listman.redhat.com/archives/redhat-ccm-list/ 2002-October/msg00081.html - 14k - [Cached](#) - [Similar pages](#)

Sponsored Links

UK People Finder

Find Lost Family & Friends Today
Fees from £35 - Featured on BBC/ITV
www.ukig.co.uk
Interest: [See your message here...](#)



US Patent & Trademark Office

[Subscribe \(Full Service\)](#) [Register \(Limited Service, Free\)](#) [Login](#)

Search: ☒ The ACM Digital Library ☐ The Guide

+embedded +tracing +("cache miss") +tag +instruction +data

SEARCH

THE ACM DIGITAL LIBRARY



[Feedback](#) [Report a problem](#) [Satisfaction survey](#)

Terms used **embedded tracing cache miss tag instruction data**

Found **162** of **125,779**

Sort results
by

relevance



[Save results to a Binder](#)

Try an [Advanced Search](#)

Try this search in [The ACM Guide](#)

Display
results

expanded form



[Search Tips](#)

☐ Open results in a new
window

Results 1 - 20 of 162

Result page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [next](#)

Relevance scale ☐ ☐ ☐ ☐ ☐

1 [Tiny instruction caches for low power embedded systems](#)

Ann Gordon-Ross, Susan Cotterell, Frank Vahid

November 2003 **ACM Transactions on Embedded Computing Systems (TECS)**, Volume 2
Issue 4

Full text available: [pdf\(887.71 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Instruction caches have traditionally been used to improve software performance. Recently, several tiny instruction cache designs, including filter caches and dynamic loop caches, have been proposed to instead reduce software power. We propose several new tiny instruction cache designs, including preloaded loop caches, and one-level and two-level hybrid dynamic/preloaded loop caches. We evaluate the existing and proposed designs on embedded system software benchmarks from both the Powerstone and ...

Keywords: Loop cache, architecture tuning, embedded systems., filter cache, fixed program, instruction cache, low energy, low power

2 [Efficient management of memory hierarchies in embedded DRAM systems](#)

Ashley Saulsbury, Su-Jaen Huang, Fredrik Dahlgren

May 1999 **Proceedings of the 13th international conference on Supercomputing**

Full text available: [pdf\(1.57 MB\)](#) Additional Information: [full citation](#), [references](#), [index terms](#)

Keywords: COMA, DRAM, cache, latency, memory hierarchy, processor

3 [Memory optimization for embedded systems: Improved indexing for cache miss reduction in embedded systems](#)

Tony Givargis

June 2003 **Proceedings of the 40th conference on Design automation**

Full text available: [pdf\(215.59 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The increasing use of microprocessor cores in embedded systems as well as mobile and portable devices creates an opportunity for customizing the cache subsystem for improved performance. In traditional cache design, the index portion of the memory address bus consists of the K least significant bits, where $K = \log_2(D)$ and D is the depth of the cache. However, in devices where the application set is known and characterized (e.g., systems that execute a fixed application set) there is an opportunity ...

Keywords: cache optimization, design space exploration, index hashing



Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

Your search matched **57** of **995179** documents.
A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.

Refine This Search:

You may refine your search by editing the current search expression or entering a new one in the text box.

☐ Check to search within this result set**Results Key:**

JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

1 On the fractal dimension of computer programs and its application to the prediction of the cache miss ratio

Thiebaut, D.;

Computers, IEEE Transactions on , Volume: 38 , Issue: 7 , July 1989

Pages:1012 - 1026

[\[Abstract\]](#) [\[PDF Full-Text \(1024 KB\)\]](#) **IEEE JNL**

2 On the use of trace sampling for architectural studies of desktop applications

Crowley, P.; Baer, J.-L.;

Workload Characterization: Methodology and Case Studies, 1998 , 29 Nov. 1998

Pages:15 - 24

[\[Abstract\]](#) [\[PDF Full-Text \(136 KB\)\]](#) **IEEE CNF**

3 Expected I-cache miss rates via the gap model

Quong, R.W.;

Computer Architecture, 1994. Proceedings the 21st Annual International Symposium on , 18-21 April 1994

Pages:372 - 383

[\[Abstract\]](#) [\[PDF Full-Text \(824 KB\)\]](#) **IEEE CNF**

4 Increasing the accuracy of statistical simulation for modeling superscalar processors

Eeckhout, L.; De Bosschere, K.;

Performance, Computing, and Communications, 2001. IEEE International Conference on. , 4-6 April 2001

Pages:196 - 204

[\[Abstract\]](#) [\[PDF Full-Text \(832 KB\)\]](#) **IEEE CNF**
